



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No.	10/766,774	Confirmation No.	8283
First Inventor	Richard K. Williams	Filing Date	01/28/2004
Tech. Center/ Art Unit	2822	Examiner	Mary A. Wilczewski
Title:	Modular Bipolar-CMOS-DMOS Analog Integrated Circuit And Power Technology		
Docket No.:	AAT007-3C US	Customer No.:	34036

Santa Clara, California  
March 4, 2005

COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, VA 22313-1450

**PRELIMINARY AMENDMENT**

Dear Sir:

Please amend the above-identified application as follows before beginning the examination thereof.

**Amendments to the Specification** begin on page 2 of this paper.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 25 of this paper.

**Amendments to the Drawings** begin on page 52 of this paper and include annotated sheets showing changes.

**Remarks/Arguments** begin on page 54 of this paper.

An **Appendix** including amended drawing figures is attached following page 54 of this paper.

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02 FC:2202

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